

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS

1. (Canceled)
2. (Canceled)
3. (previously presented) The display circuit as recited in claim 12 wherein said unencrypted key comprises an authentication key and said using comprises authenticating a source of data.
4. (previously presented) The display circuit as recited in claim 12, wherein said unencrypted key comprises a decryption key and said using comprises decrypting data.
5. (Canceled)
6. (previously presented) The display circuit as recited in claim 12, wherein said display unit comprises an analog display unit. 7.
7. (previously presented) The display circuit as recited in claim 12, wherein said display unit comprises a digital display unit.
8. (Canceled)

9. (previously presented) The display circuit as recited in claim 12, wherein when a source of data is authenticated, said authenticating is performed using said unencrypted key based on data sent and received on a path connected to said display unit.

10. (previously presented) The display circuit as recited in claim 9, wherein said path is implemented using I^2C protocol.

11. (previously presented) The display circuit as recited in claim 12, further comprising a master block external to said display unit that sends said unencrypted key-wherein when said encrypted key is sent to said master block,-said master block stores said encrypted key in said non-volatile memory.

12. (previously presented) A display circuit for use in a display unit, said display circuit comprising:

 a non-volatile memory storing a encrypted key, wherein said encrypted key is generated from an unencrypted key according to an encryption protocol; and

 an integrated circuit coupled to said non-volatile memory, said integrated circuit receiving said key in encrypted form and decrypting said key to generate a decrypted key, said integrated circuit using said decrypted key wherein said integrated circuit comprises a key encryption circuit receiving said unencrypted key, said key encryption circuit generating said encrypted key from said unencrypted key according to said encryption protocol, a key decryption circuit receiving said encrypted key and generating said decrypted key according to said encryption protocol, a receiver adapted for receiving a plurality of digital data elements encoded in a display signal, wherein said digital data elements represent a plurality of pixel data elements in an encrypted form, said plurality of pixel data elements representing an image, and a data decryption

circuit receiving said plurality of digital data elements and generating said plurality of pixel data elements, wherein said image is generated on a display screen based on said plurality of pixel data elements, and wherein said display signal is received according to TMDS format.

13. - 17. (Canceled)

18. (previously presented) An integrated circuit, comprising:

a key encryption circuit adapted to receive an unencrypted key and generating an encrypted key from said unencrypted key according to an encryption protocol;

a key decryption circuit adapted to receive said encrypted key and generate said decrypted key according to said encryption protocol;

a receiver adapted to receive a plurality of digital data elements encoded in a display signal that is received according to TMDS format and that represents a plurality of pixel data elements in an encrypted form, wherein said plurality of pixel data elements represents an image that is generated on a display screen based on said plurality of pixel data elements; and

a data decryption circuit adapted to receive said plurality of digital data elements and generate said plurality of pixel data elements.

19. (previously presented) The integrated circuit as recited in claim 18 wherein the integrated circuit is

coupled to a non-volatile memory suitable for storing the encrypted key.

20. (previously presented) The integrated circuit as recited in claim 19, further comprising:

a memory adapted to receive said encrypted key;

a port coupled to said memory, said port receiving said encrypted key from said memory and sending said encrypted key to a master block adapted to store said encrypted key in said non-volatile memory.

21. (previously presented) The integrated circuit as recited in claim 18, wherein the integrated circuit is coupled to a display unit.

22. (previously presented) The integrated circuit as recited in claim 21, where said display unit is a digital display unit.

23. (previously presented) The integrated circuit as recited in claim 21, wherein said display unit is an analog display unit.

24. (previously presented) A method of using and storing a cryptography key, comprising:

receiving an unencrypted key

generating said encrypted key from said unencrypted key according to an

encryption protocol

generating a decrypted key by decrypting the encrypted key according to said encryption protocol;

receiving a plurality of digital data elements encoded in a display signal wherein said display signal is received according to TMDS format, and wherein said digital data elements represent a plurality of pixel data elements in an encrypted form that represent an image;

decrypting said encrypted plurality of digital data elements;

generating said plurality of pixel data elements based upon said decrypted plurality of digital data elements; and

generating said image on a display screen based on said decrypted plurality of pixel data elements.

25. (previously presented) The method as recited in claim 23, further comprising:
storing an encrypted key generated from an unencrypted key according to an encryption protocol in a non-volatile memory.

26. (previously presented) Computer program product for using and storing a cryptography key, comprising:

computer code for receiving an unencrypted key
computer code for generating said encrypted key from said unencrypted key according to an encryption protocol
computer code for generating a decrypted key by decrypting the encrypted key according to said encryption protocol;
computer code for receiving a plurality of digital data elements encoded in a display signal wherein said display signal is received according to TMDS format, and wherein said digital data elements represent a plurality of pixel data elements in an encrypted form that represent an image;
computer code for decrypting said encrypted plurality of digital data elements;
computer code for generating said plurality of pixel data elements based upon said decrypted plurality of digital data elements;
computer code for generating said image on a display screen based on said decrypted plurality of pixel data elements; and
computer readable medium for storing the computer code.

Since the Applicant believes that independent claim 12 is allowable, the Applicant also believes that all claims depending therefrom are also allowable for at least the reasons stated above.

In addition, independent claims 18, 24, and 26 recite essentially the same limitations as does claim 12 albeit as an integrated circuit, method, and computer program product. Accordingly, claims 18, 24, and 26 and all of their dependent claims are also allowable for at least the reasons stated for claim 12 and its dependent claims.

CONCLUSION

In view of the foregoing, it is respectfully submitted that all pending claims are allowable. Should the Examiner believe that a further telephone conference would expedite the prosecution of this application, the undersigned can be reached at the telephone number set out below.

Respectfully submitted,
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